RED SEMICONDUCTOR

Company

Our vision is to enable AI to be everywhere, instantaneous, and secure

RED for Go!

Find Out More
(https://redsemiconductor.com/product/)

RED Semiconductor International Limited was founded in 2024 to deliver huge microprocessor performance gains through its semiconductor core IP called VISC (Versatile Intrinsic Structured Computing).

VISC is compatible with the standard RISC-V ISA and uses RED's innovative compression and sequencing technology to enable entire algorithmic routines to be optimised for parallel execution. It is highly scalable, capable of everything from embedded to high-performance computing.

By executing an entire routine holistically rather than its individual elements

By executing an entire routine holistically rather than its individual elements sequentially, VISC can deliver far more performance with reduced power consumption. VISC uses highly capable 64 x 128 deep byte-accessible registers, and its execution optimisation engine minimises or eliminates load/store accesses to external off-chip memory. Internalising execution in its sophisticated registers enables higher security/privacy because cipher data and private keys are never exposed to an external data bus. We are already working with partners to implement advanced security software on our IP and deliver the performance required for the AI age.

![RED](https://redsemiconductor.com)
MEET THE **RED** TEAM

David Calderwood

DIRECTOR OF R&D AND CHAIRMAN

Has built the teams of the leading technology companies; OpenPOWER Foundation executive board member.

James Lewis

CHIEF EXECUTIVE OFFICER

One of the UK's most prominent semiconductor business builders – founder of Oxford Semiconductor and Redux.

David Harold

CHIEF OPERATING OFFICER

Renowned microprocessor marketing strategist, former Imagination Technologies CMO.

Andrey Miroshnikov

SENIOR ENGINEER

Microprocessor architect; previously worked at Qualcomm.

Shriya Sharma

EMBEDDED SYSTEMS ENGINEER

Software and hardware specialist from Southampton University.

Manuel Segre

ALGORITHM SPECIALIST

AI and complex algorithmic mathematician from Queen Mary University, London.

Steve Evans

SENIOR COMMERCIAL ADVISER

Experienced microprocessor commercial innovation specialist; previously worked at Arm, MIPS and Imagination Technologies.

Haydn Povey

SENIOR TECH ADVISOR

Security guru – former CTO of IAR and ARM Director of Security Products and Technologies, member of IoT Security Foundation executive board.

James Tout

ADVISER

General Manager of Kioxia UK, creating processors for solid state mass storage.

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Justin Hill
A D V I S E R

Partner at international law firm Dentons, patent expert.

Ready to Collaborate?

Join us in leading the next wave of computing innovation.
Let's build the future together.

Contact Us
(https://redsemiconductor.com/contact/)

Frequently Asked Questions

▲ **Why hasn't this been done before?**

Large players in processor technology face legacy constraints. **RED** thrives by innovating where others are limited, rethinking outdated architectures to deliver cutting-edge solutions without compromise.

▼ **How customisable is VISC?**

▼ **What markets does RED focus on?**

▼ **Why should investors choose RED?**

▼ **How did RED Semiconductor get started??**

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